

## Details of the Workshop

Name of the workshop: VLSI to System design: Silicon to end application approach

Mode of Workshop: Virtual

Date: 31st July, 2023-4th August,2023

Time: 2:00 PM to 6:45 PM IST

Registration Link: [https://armltd.zoom.us/webinar/register/WN\\_15eSTNthSM6bL8ecsB01wQ](https://armltd.zoom.us/webinar/register/WN_15eSTNthSM6bL8ecsB01wQ)

Event Page: <https://community.arm.com/vlsi-to-system-design/>

## Social Media Image

Attached.

## Social Media Content

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Get ready to embark on an exciting journey into the world of # VLSI and #SoC technologies.

Join us on our 5-day virtual workshop with @EduMinOfIndia, @AICTE\_INDIA ,@ArmEducation, @Arm, @ST\_World, @CadenceIndia. Register here: <https://bit.ly/3NquV7y>

### For Facebook:

Build your knowledge and expand your expertise in our virtual Faculty Workshop organized by @Arm Education, @Arm, @ST Microelectronics, @Cadence India. This transformative learning experience will enable you to gain insights into the latest trends and advancements in VLSI.

To Elevate your teaching skills and empower your students with industry-relevant knowledge, Register here: <https://bit.ly/3NquV7y>

### For LinkedIn:

Ignite your passion for cutting-edge semiconductor technology at our exclusive virtual faculty workshop organized by @Arm, @AICTE, [STMicroelectronics](#), [Cadence Design Systems](#) on VLSI. Join us as we dive deep into the world of integrated circuits, digital design, and advanced EDA tools. Gain valuable insights, enhance your teaching techniques, and stay ahead of the curve in this rapidly evolving field.

Don't miss this opportunity to network with fellow educators and industry experts.

Places are limited – register here: <https://bit.ly/3NquV7y>

## Username to tag in social media

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Tag words:

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#VLSIWorkshop #VLSI #FacultyWorkshop #VLSITraining #VLSIEngineering #Semiconductor #DigitalDesign  
#IntegratedCircuits #EDA (Electronic Design Automation) #HardwareDesign #ASIC (Application-Specific  
Integrated Circuit) #FPGA (Field-Programmable Gate Array) #CMOS (Complementary Metal-Oxide-  
Semiconductor) #EDATools #VHDL (VHSIC Hardware Description Language) #Verilog #VLSIDesign  
#SystemVerilog #VLSIResearch #VLSICommunity #FacultyWorkshop #ProfessionalDevelopment  
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